

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Tatsuya Arao et al.  
Serial No. : 10/622,584  
Filed : July 21, 2003

Art Unit : 2815  
Examiner : N. Drew Richards  
Confirmation No.: 4703  
Notice of Allowance Date: April 13, 2005

Title : METHOD OF MANUFACTURING SEMICONDUCTOR DEVICE

**MAIL STOP ISSUE FEE**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450


**RESPONSE TO NOTICE OF ALLOWANCE**

In response to the Notice of Allowance mailed April 13, 2005, enclosed are a completed issue fee transmittal form PTOL-85b and a check for \$1730 for the required issue fee and publication fee, including patent copies.

Please apply any additional charges or credits to our Deposit Account No. 06-1050.

Respectfully submitted,

Date: July 13, 2005

  
\_\_\_\_\_  
John F. Hayden  
Reg. No. 37,640

Customer No. 26171  
Fish & Richardson P.C.  
1425 K Street, N.W., 11th Floor  
Washington, DC 20005-3500  
Telephone: (202) 783-5070  
Facsimile: (202) 783-2331